# Map2MPSoC +SCOPES

## **Proceedings of the**

5th Workshop on Mapping of Applications to MPSoCs

and

15th International Workshop on Software and Compilers for Embedded Systems

Map2MPSoC/SCOPES 2012

www.scopesconf.org

Copyright © 2012 by the Association for Computing Machinery, Inc (ACM). Permission to make digital or hard copies of portions of this work for personal or classroom use is granted without fee provided that the copies are not made or distributed for profit or commercial advantage and that copies bear this notice and the full citation on the first page. Copyrights for components of this work owned by others than ACM must be honored. Abstracting with credit is permitted.
To copy otherwise, to republish, to post on servers or to redistribute to lists, requires prior specific permission and/or a fee. Request permission to republish from: Publications Dept. ACM, Inc. Fax $+1-212-869-0481$ or E-mail permissions@acm.org.
For other copying of articles that carry a code at the bottom of the first or last page, copying is permitted provided that the per-copy fee indicated in the code is paid through the Copyright Clearance Center, 222 Rosewood Drive, Danvers, MA 01923.

### Proceedings of the

## 5th Workshop on Mapping of Applications to MPSoCs

and

## 15th International Workshop on Software and Compilers for Embedded Systems

Map2MPSoC/SCOPES 2012

May 15-16, 2012 Schloss Rheinfels St. Goar, Germany

Sponsors

**EDAA** 

In cooperation with

**ACM SIGBED** 

Editor

Sander Stuijk, Eindhoven University of Technology, The Netherlands







### **Table of Contents**

reface	iii
ommittee	v
ponsors	vii
eynote I: Reconfigurable multi-cores for streaming DSP	1
eynote II: A Performance Comparison of HEVC Parallelization Proposalsen Juurlink	2
LASSY: a Clock Analysis System for Rapid Prototyping of Embedded Applications on MPSoCs in An, Sarra Boumedien, Abdoulaye Gamatie and Eric Rutten	3
ecoupled inter- and intra- application scheduling for composable and robust embedded SoC atforms	13
nca Molnos, Ashkan Beyranvand Nejad, Ba Thang Nguyen, Kees Goossens and Sorin Cotofana	
xploration of Heuristic Scheduling Algorithms for 3D Multicore Processorshomas Canhao Xu, Pasi Liljeberg, Juha Plosila and Hannu Tenhunen	22
ardware Virtualization-driven Software Task Switching in Reconfigurable Multi-Processor vstem-on-Chip Architectures	32
ebugging Component-Based Embedded Applicationsevin Pouget, Miguel Santana, Vania Marangozova-Martin and Jean-François Mehaut	42
ast Architecture Evaluation of Heterogeneous MPSoCs by Host-Compiled Simulation	52
ynamic Method to Evaluate Code Optimization Effectiveness	62

#### **Preface**

Dear Colleague,

Welcome to Sankt Goar and the Map2MPSoC/SCOPES workshop. After having been co-located for four years, the workshop on Mapping of Applications to MPSoCs (Map2MPSoC) and the workshop on Software and Compilers for Embedded Systems (SCOPES) have joint forces. This year we are presenting a combined Map2MPSoC/SCOPES workshop program that features many interesting talks on all aspects related to the design of modern embedded systems. I hope that you will find our program interesting, stimulating and exciting.

The influence of embedded systems is constantly growing. Increasingly powerful and versatile devices are developed and put on the market at a fast pace. Their functionality and number of features is increasing, and so are the constraints on the systems concerning size, performance, energy dissipation and timing predictability. To meet all these constraints, multi-processor systems on a chip (MPSoCs) are becoming popular in embedded systems. In order to meet the performance and energy constraints of embedded applications, heterogeneous architectures incorporating functional units optimized for specific functions are commonly employed. This technological trend has dramatic consequences on the parallelization, mapping, compiler and design technology used to develop these systems. The Map2MPSoC/SCOPES workshop focuses on the software generation process for these modern embedded systems. Topics of interest include all aspects of the compilation and mapping process of embedded single and multi-processor systems.

Map2MPSoC/SCOPES received a total of 17 papers coming from many different countries in Europe, North- and South-America, Asia, and Australia. Each paper has been reviewed by at least four independent reviewers to ensure the quality of the workshop. Each reviewer provided a score together with detailed comments and suggestions on how to improve the overall quality of each paper. After an on-line meeting, the program committee has decided to accept 7 papers out of these 17 submissions. This gives an acceptance rate of 41% which is in-line with earlier editions of the Map2MPSoC and SCOPES workshops. It also reflects our commitment to only select high quality papers for presentation at our workshop.

In addition to the research papers, the workshop features also 7 research presentations. The idea of research presentations was previously used at the Map2MPSoC workshop and has been continued in the joint workshop program. Research presentations show research results relevant to the topics addressed by the workshop. These presentations may be based on on-going work or research results that have previously been presented in other forums. Research presentations do not include a publication in the Map2MPSoC/SCOPES proceedings, but they should stimulate discussion at the workshop.

In conclusion, I would like to thank the members of the program committee and the external reviewers for their contribution to the quality of this workshop. I would also like to thank all authors for choosing Map2MPSoC/SCOPES as the workshop where to report your research and your contributions to the scientific community. Finally, I would like to thank our sponsors for their support to Map2MPSoC/SCOPES 2012. I wish all of you a fruitful conference and a pleasant stay in Sankt Goar.

Sander Stuijk Map2MPSoC/SCOPES 2012 Program Chair Eindhoven University of Technology, NL s.stuijk@tue.nl

#### **Committee**

#### • General Chair

Henk Corporaal Eindhoven University of Technology, NL

#### • Program Chair

Sander Stuijk Eindhoven University of Technology, NL

#### • Publicity Chair

Peter Marwedel
Dortmund University of Technology, DE

#### • Program Committee

- Iuliana Bacivarov
   ETH Zurich, CH
- Marco Bekooij
   NXP Semiconductors, NL
- Koen de Bosschere Ghent University
- Nikil Dutt
   University of California, Irvine, USA
- Heiko Falk
   Ulm University, DE
- Soheil Ghiasi
   University of California, Davis, USA
- Christian Haubelt University of Rostock, DE
- Timothy Jones
   University of Cambridge, UK
- Andreas KrallTU Wien, AT
- Akash Kumar
   National University of Singapore, SG

- Christian Lengauer
   University of Passau, DE
- Rainer Leupers
   RWTH Aachen University, DE
- Bilha Mendelson
   IBM research Lab, IL
- Anca Molnos
   Delft University of Technology, NL
- Yunheung Paek
   Seoul National University, KR
- Andy Pimentel
   University of Amsterdam, NL
- Chung-Ching Shen
   University of Maryland, USA
- Todor Stefanov Leiden University, NL
- Sander Stuijk
   Eindhoven University of Technology, NL
- Jürgen Teich
   University of Erlangen, DE

#### • External Reviewers

- Gergö Barany
- Jeronimo Castrillon
- Anup Das
- Armin Groesslinger
- Jovana Jovic
- Nikolai Kim
- Lars Middendorf
- Hyungon Moon
- Rajesh Panicker

- Projjol Ray
- Sascha Roloff
- Andreas Simbürger
- Amit Singh
- Alexandru Tanase
- KazuyukiTanimura
- Andreas Weichslgartner
- Christian Zebelein

### **Sponsors**

Map2MPSoC/SCOPES 2012 is kindly supported and sponsored by the following institutions:

• ACM SIGBED

http://www.acm.org/sigbed

• European Design and Automation Association, EDAA

http://www.edaa.com





